REMARKS

Receipt of the Office Action of October 19, 2007 is gratefully acknowledged.

New claims 4 and 5 have been examined and have been rejected as follows: under 35 USC 101 "because the claimed invention is directed to non-statutory subject matter; and under 35 USC 102(b) as anticipated by Shang et al.

Regarding the rejection under 35 USC 101 claims 4 and 5 have been amended to provide the concrete structure which should satisfy the requirements of 35 USC 101 as this statutory provision has been interpreted by the case law. Specifically, "entry"/"entries" have been changed to "register"/"registers," and "(entry) address" has been changed to "(register) number."

These amendments to claims 4 and 5 should obviate the rejection under 35 USC 101.

Regarding the rejection under 35 USC 102(b) by Shang, it is noted that the examiner refers to col. 3, lines 50 - 67 through col. 4, lines 1 - 6 and col. 14, lines 9 - 30 of Shang for a disclosure of modifications to an operand stack, wherein the existence of the TOS (top of the stack) register is presupposed. In fact Shang states in col. 14, lines 9 - 24: "Although specific embodiments have been illustrated and described, it will be obvious to those skilled in the art that various modifications may be made without departing from the spirit which is intended to be limited solely by the appended claims. For example,..... Although the TOS is assumed to point to the stack top element, alternate designations can be used to represent the TOS differently, such as the TOS may be designated to point to an empty location adjacent to the stack top element such that the relative locations for operands and the stack top locations are subtracted by one." In the

context of Shang's system, or the like, it is not "the number of the register of the advanced mapping file" but the distance from the TOS "to indicate the number of operand stack elements over the operand stack element." Therefore, Shang discloses neither specific embodiments nor their modifications in accordance with claims 4 and 5 of the present application.

With respect to the preferred embodiment disclosed in the present application, "Such a state of the operand stack of a traditional stack machine as {..., word3, word2, word1} (the right end is the top of the stack) corresponds to a state of the computer system of the present invention in which, with a, b, c,...representing contents of mapping - file entries of address 0, 1, 2,...respectively, word1, word2, word3,....are (to be) held in the data-file entries whose addresses are a, b, c,.....respectively" (page15, lines 6 - 11). Namely, values of the top, 2nd, 3rd,.....element of the operand stack are (to be) held in the data-file entries whose addresses are indicated in the advanced-mapping-file entries of address 0, 1, 2,.....respectively. Therefore, a TOS register or the like is unneeded, since the entry (register) of the advanced-mapping-file corresponding to the top of the stack stays unchanged despite growth/shrinkage of the stack.

The structure of the advanced-mapping-file (AMF) of the preferred embodiment is described on page 14, line 23 to page 16, line 13 and Fig. 3.

The actual action of the preferred embodiment concerning claims 4 and 5 is described on page 23, line 21 to page 24, line 21 and also page 8, line 11 to page 11, line 7. And, an example action concerning claims 4 and 5 is described on page 28, line 1 to page 31, line 2 and Figs. 8 - 10.

By adopting the look-ahead stack management system according to claims 4 and 5, the circuit for making a modification on the advanced-mapping-file can

be streamlined. It is not possible, it is respectfully submitted, to achieve this result with the teachings found in Shang.

In view of the foregoing, entry of the above-noted amendments to claims 4 and 5 is respectfully submitted and claims 4 and 5 indicated as allowed, alternatively entry is requested for purposes of appeal.

Respectfully submitted,

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Felix J. D'Ambrosio
Reg. No. 25,721

BACON & THOMAS, PLLC
625 Slaters lane - 4th Floor
Alexandria, VA 22314
Tel: (703)683-0500
Fax:(703)683-1080